

WHAT IS CLAIMED IS:

1. A method of removing pattern defects in a liquid crystal display, wherein the liquid crystal display comprises:
 - a gate wire on a substrate, the gate wire including a gate electrode and a gate line;
 - a gate insulating layer covering an exposed surface of the substrate, including the gate wire;
 - a data wire on the gate insulating layer, the data wire including a data line, a source electrode, and a drain electrode;
 - an active layer on the gate insulating layer, the active layer constituting a thin film transistor with the gate, source, and drain electrodes;
 - a passivation layer covering an exposed surface of the substrate, except for an exposed portion of the data wire; and
 - a pixel electrode on the passivation layer, the pixel electrode connected to the exposed portion of the data wire,
- the method comprising: ~
 - forming an etch-stop layer covering the data wire and the thin film transistor;
 - exposing at least one pattern defect by etching the passivation layer, using the etch-stop layer and pixel electrode as an etch mask;
 - removing the pattern defect; and
 - removing the etch-stop layer.

2. The method of removing pattern defects in a liquid crystal display according to claim

2 1, wherein the etch-stop layer covers the pixel electrode.

1 3. The method of removing pattern defects in a liquid crystal display according to claim
2 2, wherein the pattern defect is generated from residue of a substance used to form the active
3 layer.

1 4. The method of removing pattern defects in a liquid crystal display according to claim
2 2, wherein the pattern defect is generated from residue of a substance used to form the data
3 wire.

1 5. The method of removing pattern defects in a liquid crystal display according to claim
2 3, wherein the pattern defect is removed by etching.

1 6. The method of removing pattern defects in a liquid crystal display according to claim
2 1, wherein the data wire includes a data pad connected to an end of the data line.

1 7. A liquid crystal display comprising:
2 a substrate;
3 a gate wire on the substrate, the gate wire including a gate electrode and gate line;
4 a gate insulating layer covering an exposed surface of the substrate including the gate
5 wire;
6 a thin film transistor formed in an active layer on the gate insulating layer, the thin
7 film transistor having the gate electrode and further having a source electrode and a drain
8 electrode;

9 a data wire on the gate insulating layer, the data wire including a data line, the source
10 electrode, and the drain electrode;

11 a pixel electrode connected to the drain electrode of the thin film transistor; and

12 a passivation layer covering the data wire and the thin film transistor, except the drain
13 electrode, the passivation layer being covered by the pixel electrode, the passivation layer
14 exposing the gate insulating layer except portions of the gate insulating layer where the data
15 wire, the thin film transistor, and pixel electrode are formed.

1 8. The liquid crystal display according to claim 7, further comprising:

2 a data pad at an end of the data line, said data pad being covered with the passivation
3 layer;

4 a contact hole in the passivation layer, the contact hole exposing an exposed portion
5 of the data pad; and

6 a data pad covering layer covering the exposed portion of the data pad.

1 9. The liquid crystal display according to claim 7, wherein a part of the data wire on the
2 gate insulating layer over the gate line comprises a subsidiary electrode, and wherein the
3 subsidiary electrode comprises an exposed portion and a remainder portion being covered
4 with the passivation layer, and wherein the exposed portion of the subsidiary electrode is
5 connected to the pixel electrode.

1 10. The liquid crystal display according to claim 7, wherein the passivation layer covers
2 the data wire and thin film transistor, and exposes a portion of the pixel electrode.